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Patent

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| In re Application of: P. Patra, et al.) | |
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| j | Examiner: Unknown |
| Application No.: 10/665,521 | Art Unit: 2825 |
| Filing Date: September 22, 2003 | I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissions |
| For: Time Synthesis for Power Optimization of High) | for Patents, PO Box 1450, Alexandria, Virginia 22313-1450 |
| Performance Circuits) | on 3/10/04 |
| Commissioner for Patents | Delva L. Milaurne |
| P.O. Box 1450 | Name of Person Mailing Correspondence |
| Alexandria, Virginia 22313-1450 | DIMANT Multirerae 3/10/04 |
| , • | Signature |

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The assignee, Intel Corporation, of the above-identified Patent Application, hereby revokes all previous powers of attorney given in this Patent Application, and appoints the firm identified below and individual.

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The individual whose signature appears below is authorized to execute this Power of Attorney on behalf of Intel Corporation.

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Date: March 10, 2004

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